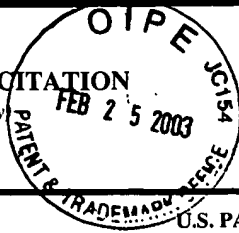


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Docket Number (Optional)

DE920000006US1

Application Number

09/864,590

Applicant(s)

Harry Barowski et al.

Filing Date

5/24/01

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SG	A	5222767	8-15-1995	Eickemeyer et al.	395	401	10-23-1992
SG	B	5996060	11-30-1999	Mendelson et al.	712	205	9-25-1997

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REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

SG	C	"Highly Accurate Data Value Prediction Using Hybrid Predictors", K. Wang et al., Proceedings of the 30th Annual ACM/IEEE International Symposium on Microarchitecture, IEEE, 1997, pp. 281-290
SG	D	"Global Context-Based Value Prediction", T. Nakra et al., Proceedings of the Fifth International Symposium on High-Performance Computer Architecture, IEEE, 1999 pp. 4-12

EXAMINER

Shane Sene

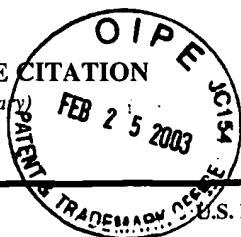
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							YES	NO

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SG	E	"Value Prediction for Speculative Multithreaded Architectures", MICRO-32, Proceedings of the 32nd Annual ACM/IEEE International Symposium on Microarchitecture, IEE International Comp. Soc., 1999, pp. 230-236
SG	F	"The Predictability of Data Values", Y. Sazeides et al., Proceedings of the 30th Annual ACM/IEEE International Symposium on Microarchitectures, IEEE Comp. Soc., 1997, pp. 248-258

EXAMINER

Shane Smith

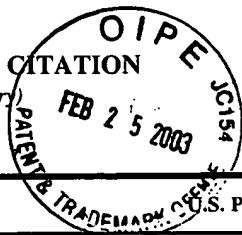
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56	G	"Architecture of the Atlas Chip-Multiprocessor: Dynamically Parallelizing Irregular Applications", L. Codrescu et al., IEEE Transaction on Computers, Vol. 50, No. 1, January 2001, pp. 67-82

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Shane Shore

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